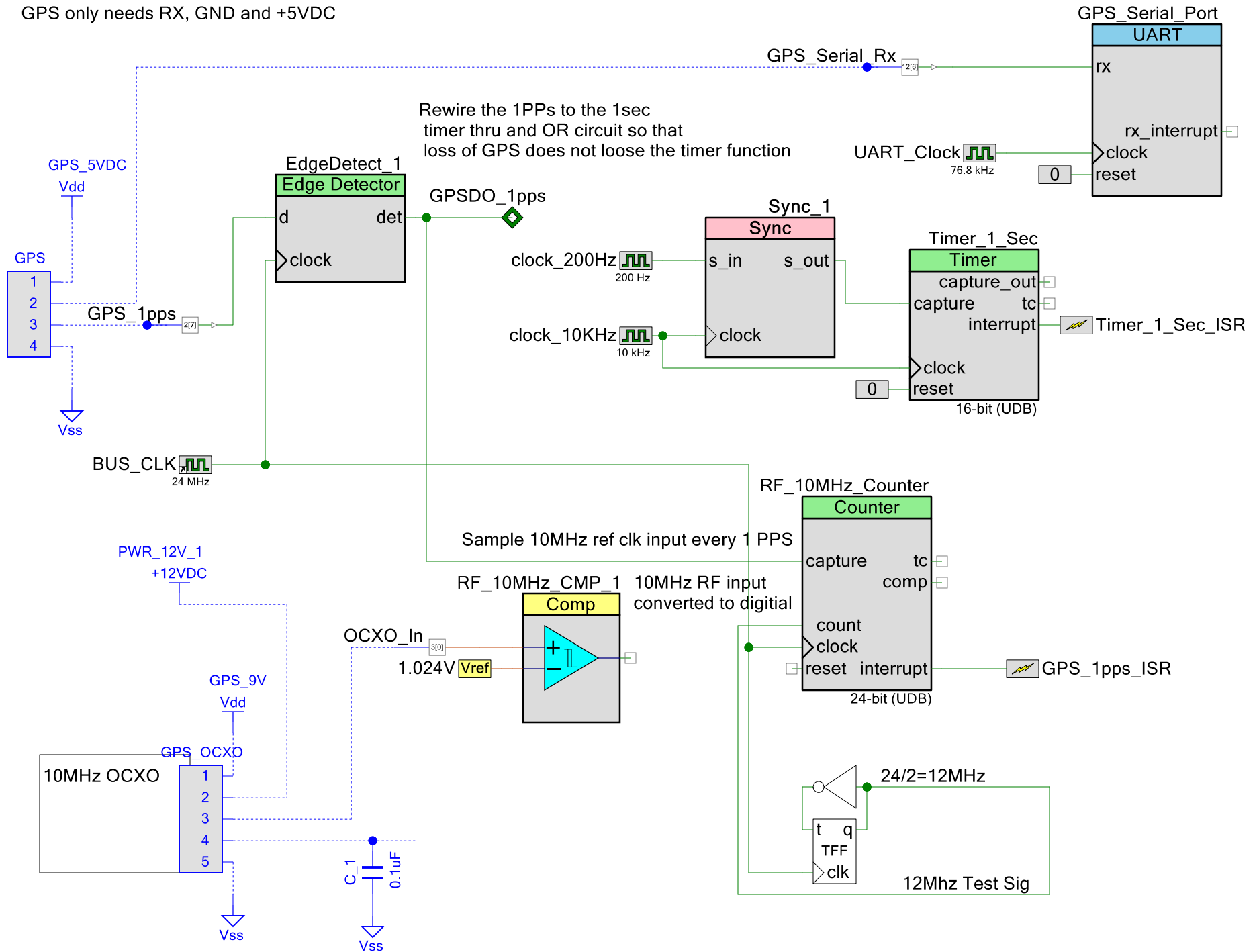
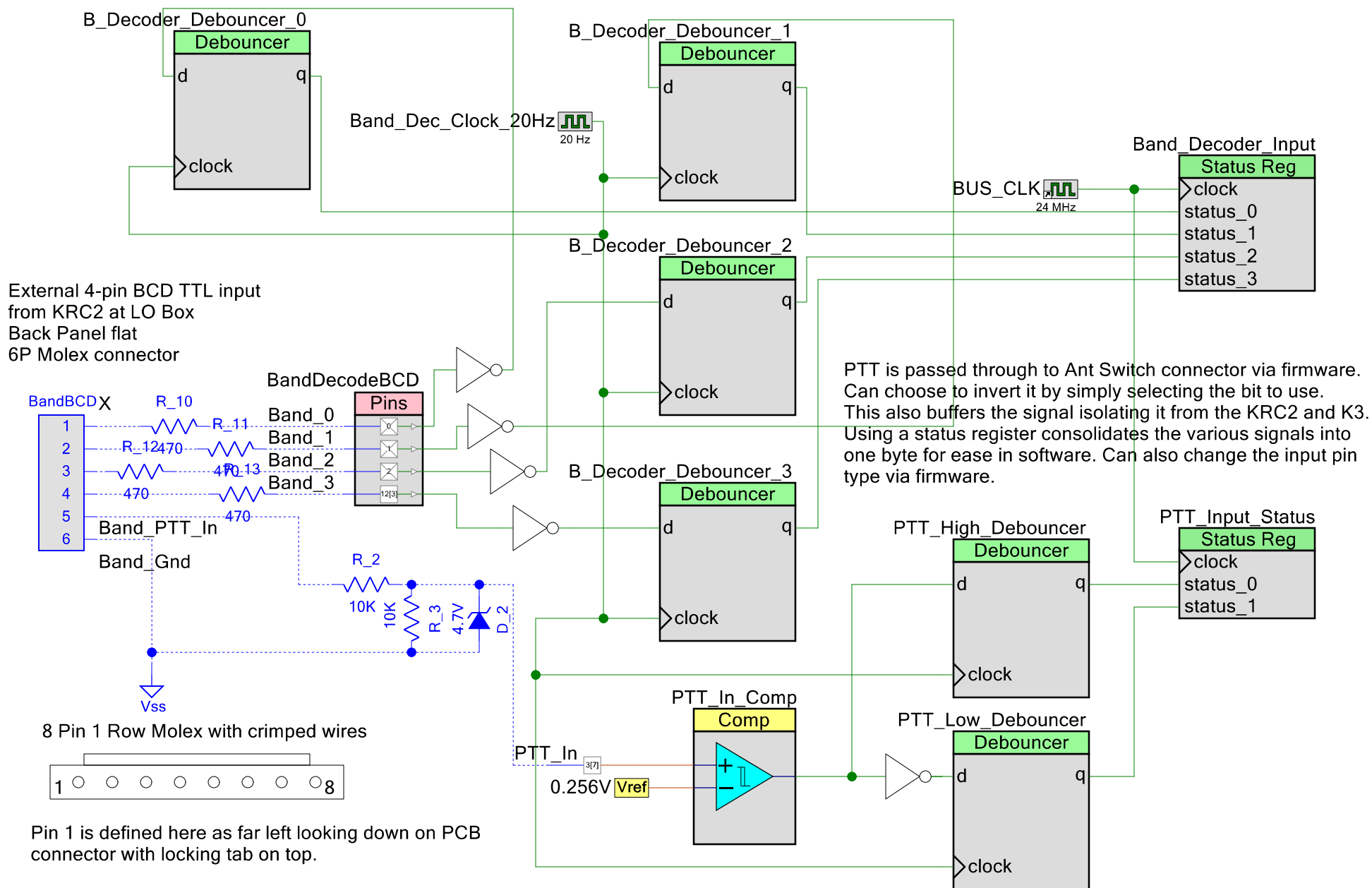


GPS only needs RX, GND and +5VDC



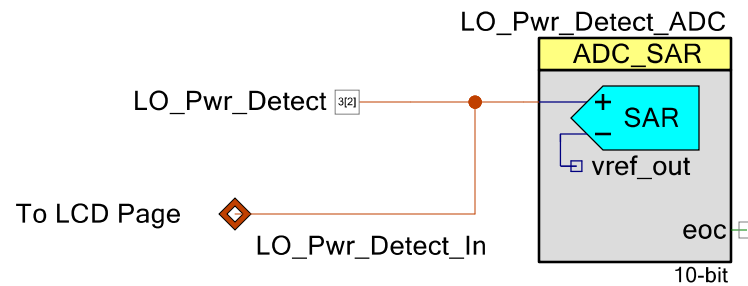


\*\*\* Note: \*\*\*

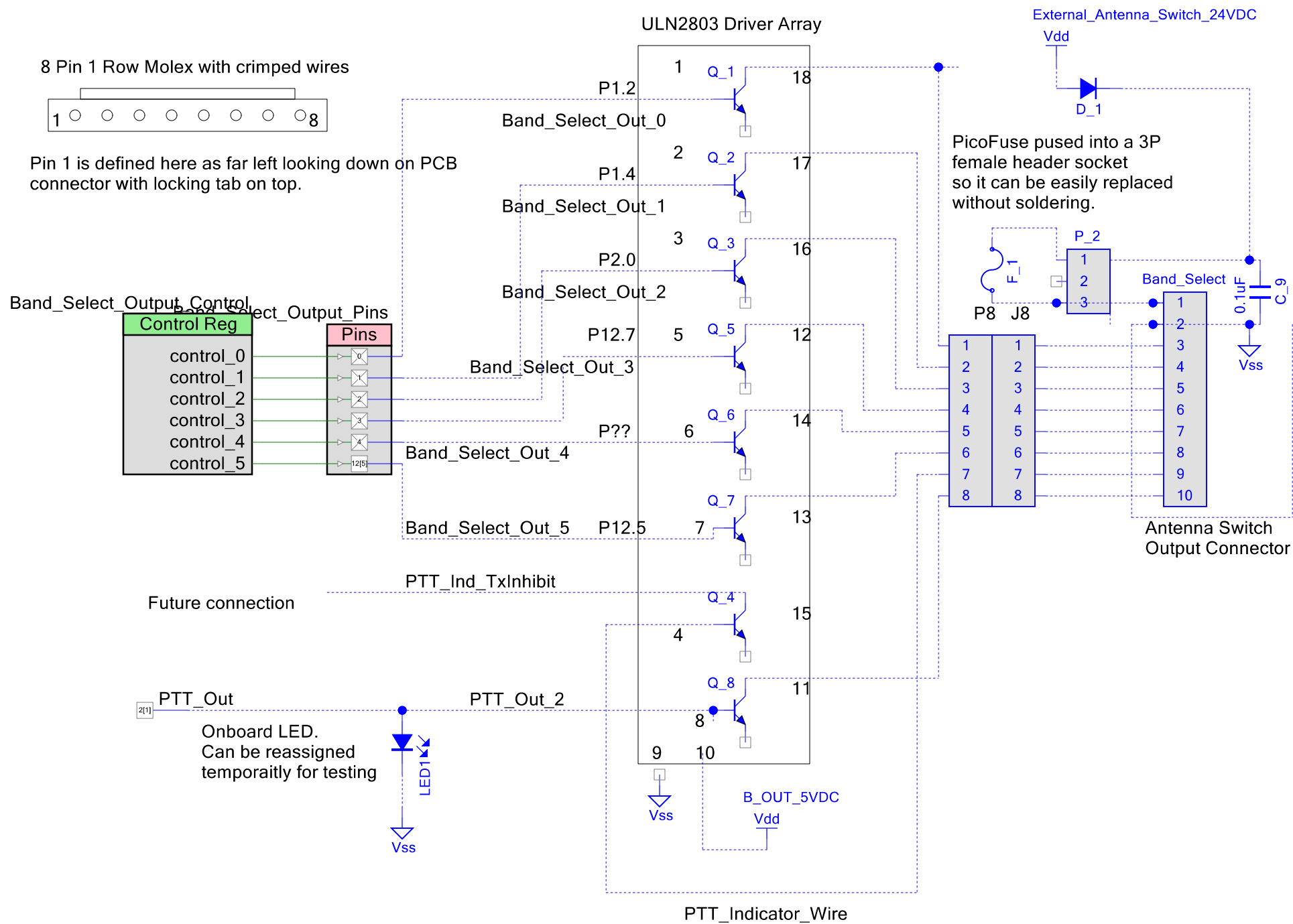
Currently the 2 band select pins are wired to the BandBCD plug

Band Decoder Input from KRC2

CPU PCB

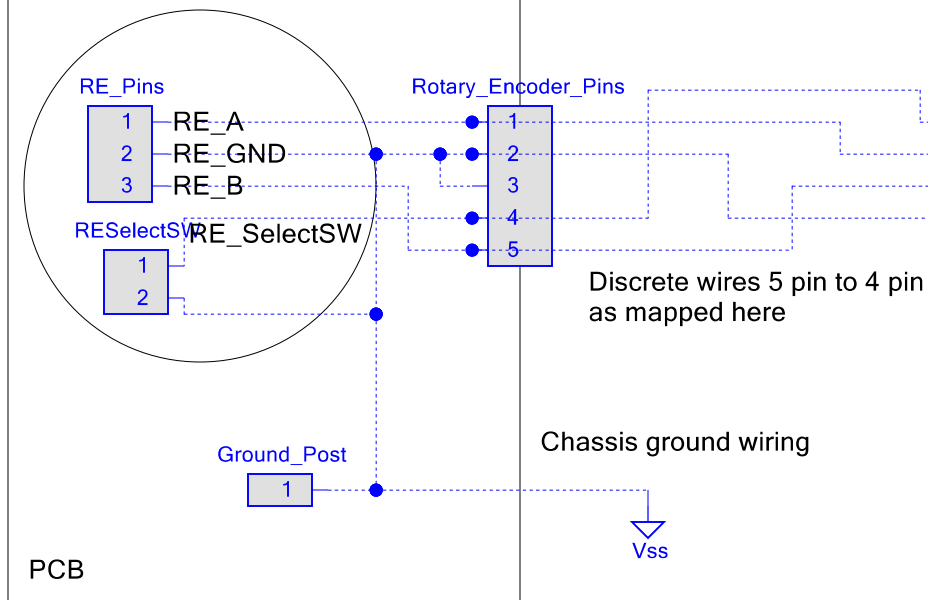


Read 0dBm to -55 dBm for 8GHz log power detector board.  
 25mV per dB. .5V to 2.1V output range.  
 2.1 is no signal. 0.5V is +5dBm  
 Use 30dB attenuator before detector as full LO ouput can reach +22dBm  
 10bit is 1024 counts with 2.048V ref V = about 2mV per count  
 thus each count



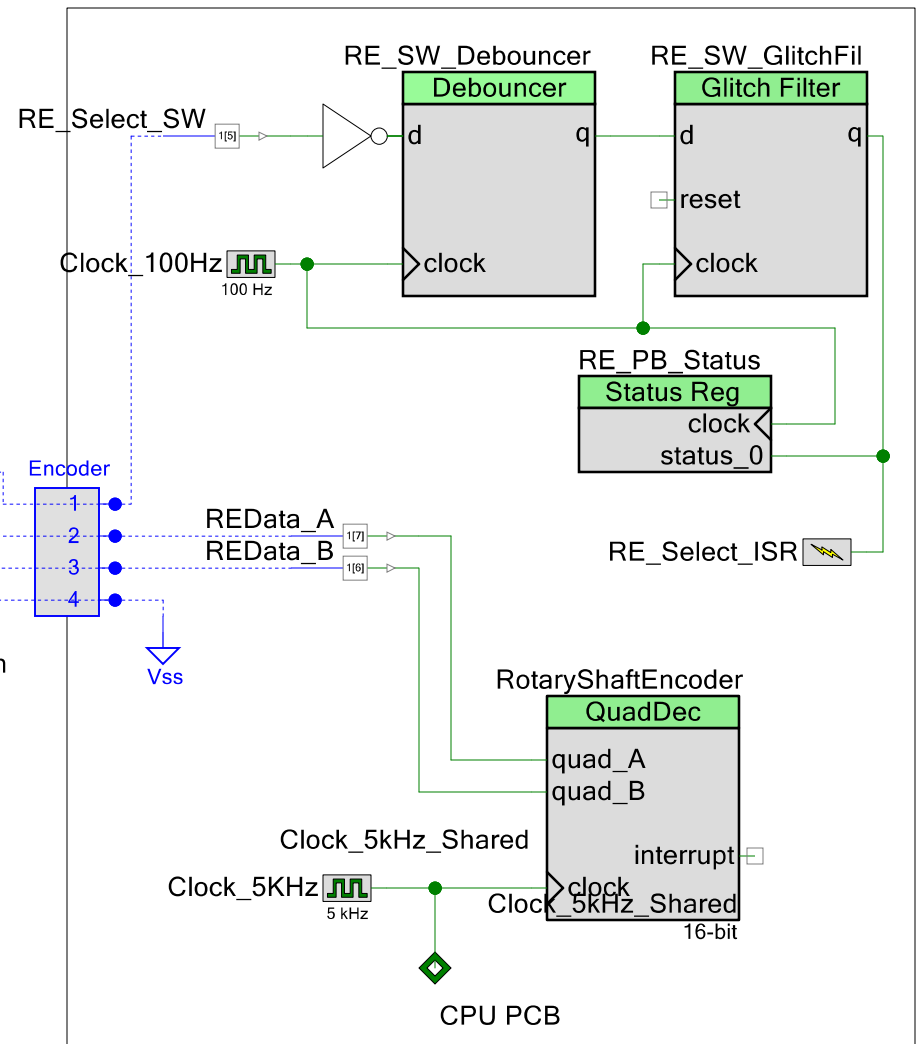
## Encoder PCD Mounted to back of Encoder Switch

Rotary Encoder with PB switch  
Center Pin of Encoder is GND  
connected to one side of PB switch



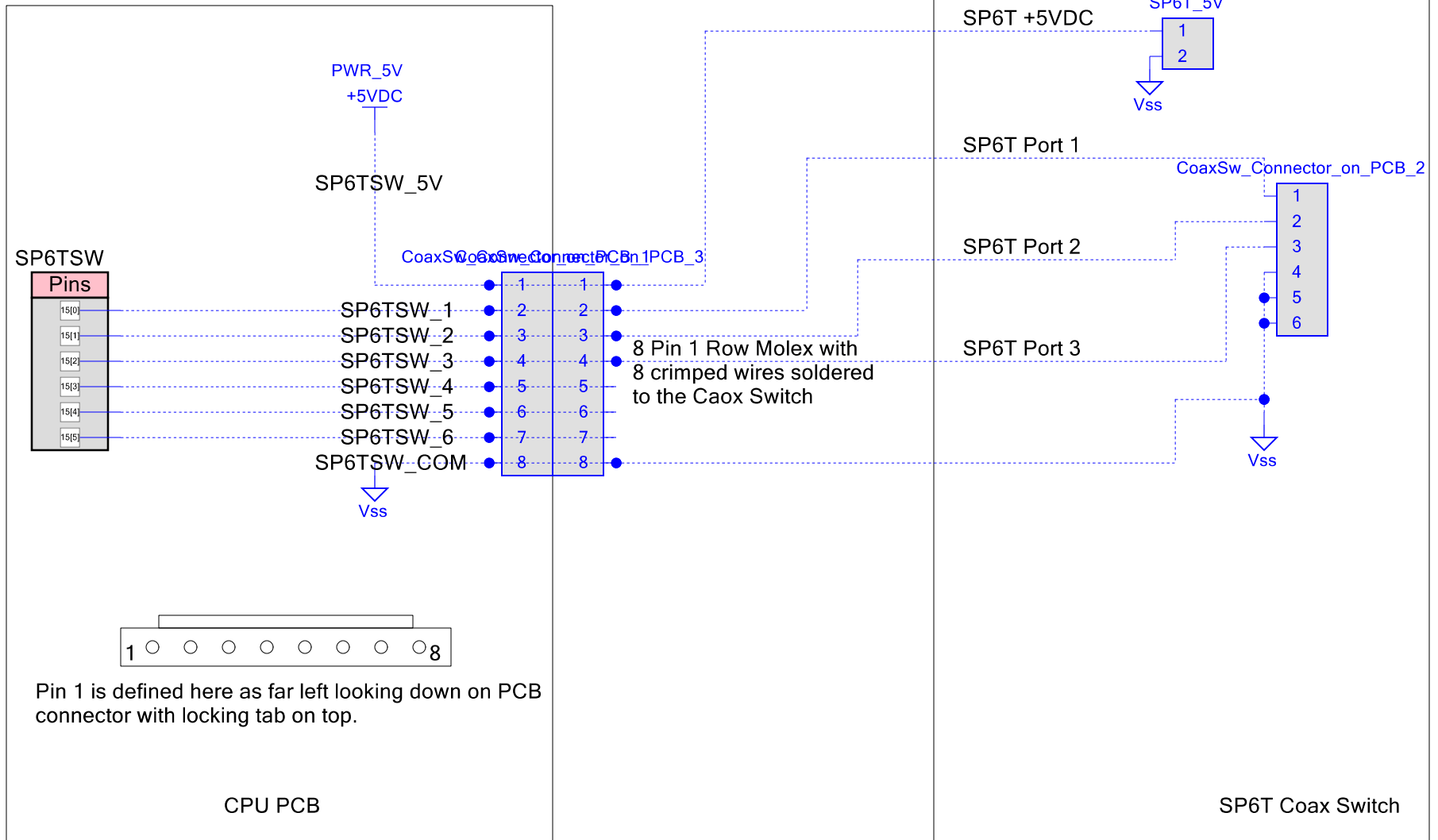
Header pin on PCB soldered to  
Encoder body ground mounting pin

Used as convenient chassis ground  
connection on front panel for nearby  
power switches and LEDs.

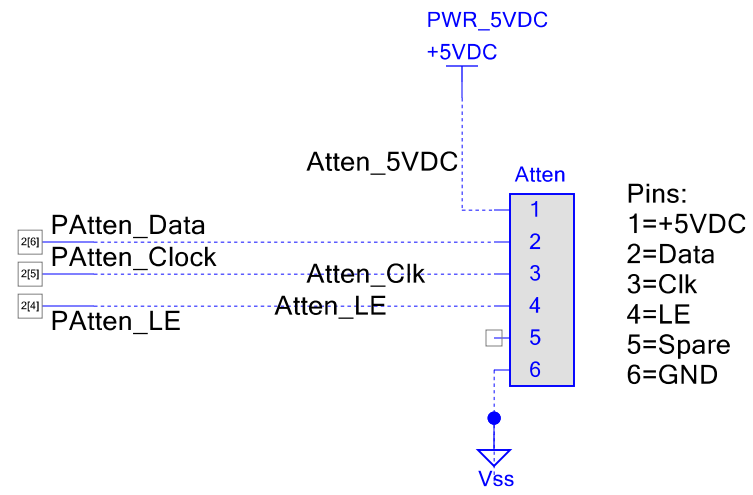


SP6T TTL controlled 12VDC Coaxial  
Switch mounted on back Panel  
About 240ma when slected.

\*\*\*When not in use set all ports to Off to save power.

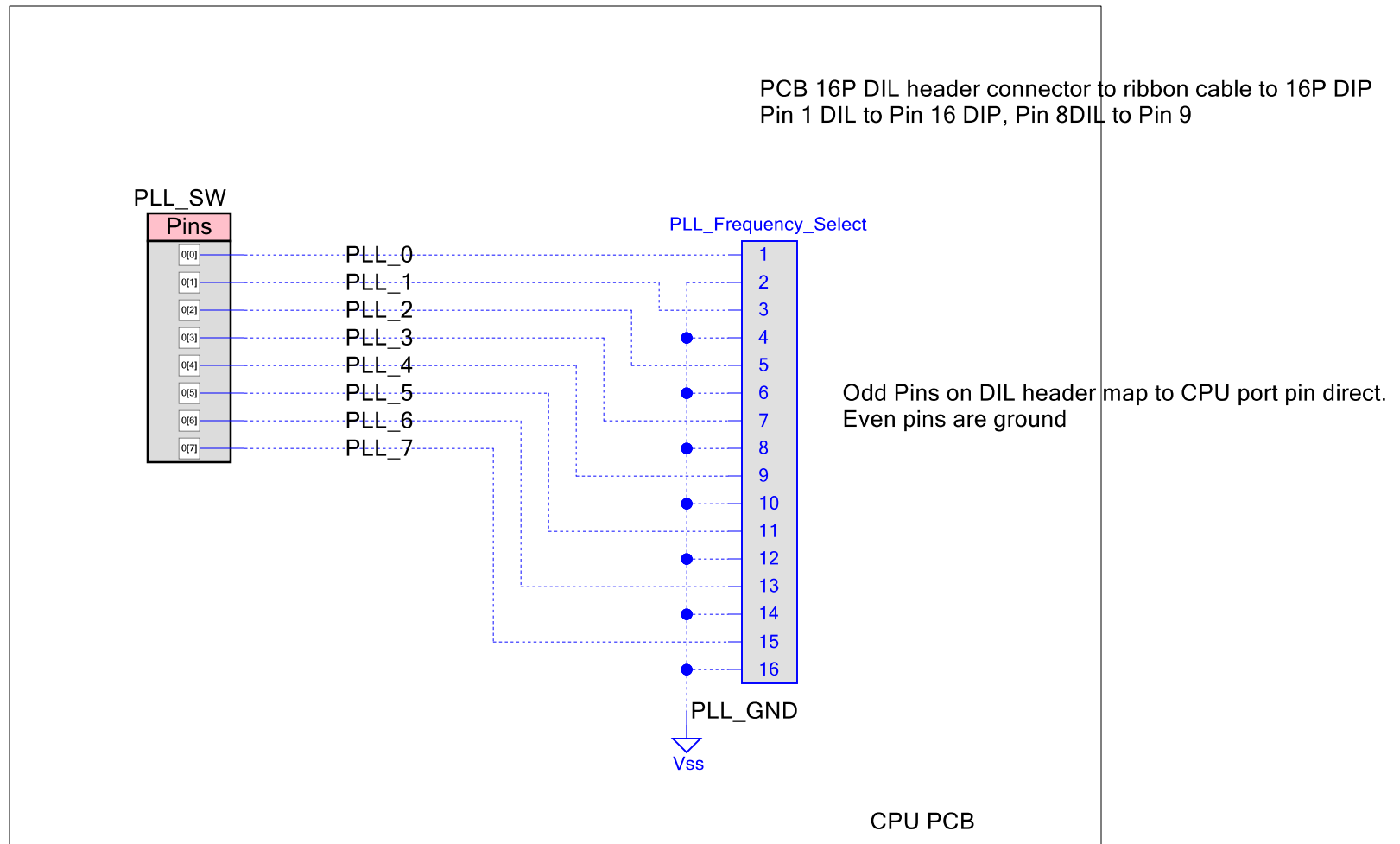


Solid State PE43703 Solid State Programmable Attenuator good to 6GHz. SPI bus.  
31.75dB range in 0.25dB steps. Only using 1 dB steps.



## DigiLO Programmable PLL Frequency Select

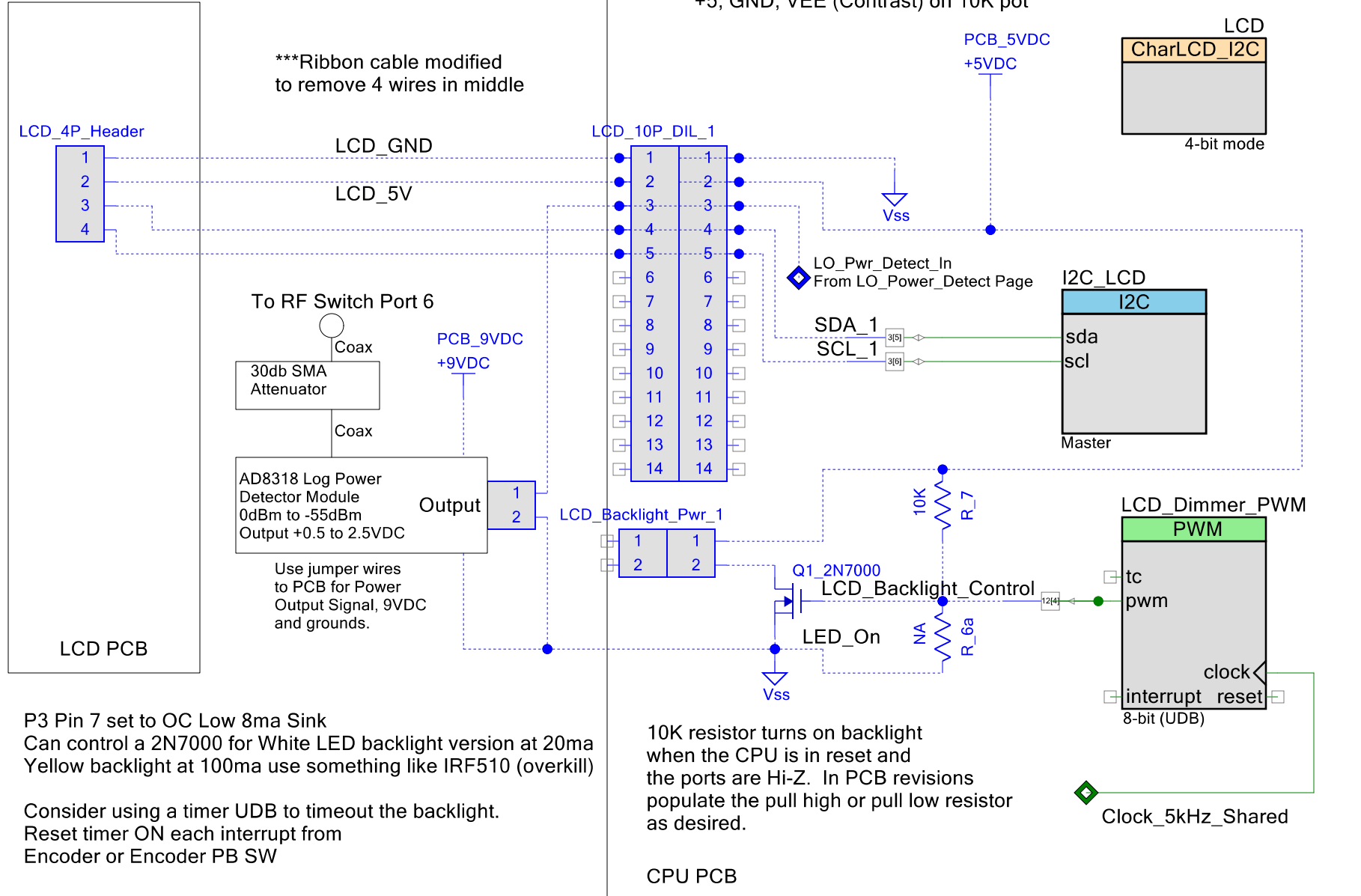
Refer to DigiLo DIP switch frequency mapping assignments in datasheet.  
For example, 404MHz is LO for 432MHz, ground DIP switch 1 (0-7).  
This translates to ribbon cable wire #3.

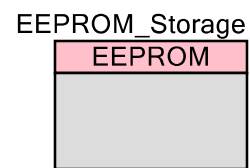




## LCD Display Connection

I2C LCD interface  
for 2x16 or 4x20 displays





Onboard Test switch hard wired to Port P\_2  
Can be reassigned in firmware to test purposes  
but normally in use as the 32/64dB attenuator control line

